

Craig Kief has over 20 years experience in various computer and communications areas including voice and data networks, testing, troubleshooting, debugging, system administration, documentation, embedded software, interfacing between software and hardware, and network monitoring. Craig Kief serves as the Deputy Director of the FPGA Mission Assurance Center (FMAC). The FMAC is responsible for advancing the areas of programmable logic to include: ensured success of FPGA projects, substantial New Mexico business growth, steady flow of competent designers into the workforce and improved design flow to speed up projects. The Center contains and hosts training and laboratory development areas that specialize in the area of advanced FPGA research. Craig retired after 20 years in the Air Force working in the areas of satellite communications, networking and ground RF systems. His last assignment was with the Air Force Operational Test and Evaluation Center.

Craig is also a member of a team developing an FPGA based laser scene generator (LADAR) under contract to the Air Force Research Laboratory. In this capacity he is responsible for code and hardware design, simulation and development. Craig has extensive background in programmable logic involving FPGA and CPLD technologies. He designed and built an eight layer PCB for his Masters Project. He has a B.S. and M.S. in Computer Engineering from University of New Mexico and currently serves as a Research Scholar on the faculty in these areas. Craig has published and taught in areas of digital and programmable logic and in areas of Verification, Validation and Accreditation of Systems. He is a Certified Professional (Modeling and Simulation Professional Certification Commission).